

Georges Gielen announced as founding Editor-in-Chief of *Foundations and Trends in Integrated Circuits and Systems*



Georges G.E. Gielen has been announced as the founding Editor-in-Chief of *Foundations and Trends in Integrated Circuits and Systems*, with the first issues due to be published in 2021. As Editor-in-Chief, it is Prof. Gielen's goal to publish fundamental and defining work, overview and tutorial articles, as well as future trends, from leading researchers within the Circuits and Systems and Solid-State Circuits communities, amongst others.

Georges G.E. Gielen is professor in the MICAS research group of the Department of Electrical Engineering since 1993 and Full Professor since 2005. In 1990-1991, he was visiting lecturer and postdoctoral researcher at the Department of Electrical Engineering and Computer Science of the University of California, Berkeley. From 2007 till 2012 he was the Head of the MICAS research group. In 2012 he became the Chair of the Department and from 2013 till 2017 he served as Vice-rector of the University of Leuven responsible for the entire group Science, Engineering and Technology. He was the PI coordinator of the Leuven CHIPS Center of Excellence on microelectronic IC design.

His research interests are in the design of analog and mixed-signal integrated circuits (sensor interfaces, data converters...), and especially in analog and mixed-signal CAD tools and design automation (modeling, simulation and symbolic analysis, analog synthesis, analog layout generation, analog and mixed-signal testing). He is a frequently invited keynote speaker and course lecturer at universities and companies worldwide, and is coordinator or partner of several (industrial) research projects in this area. He has (co-)authored more than 600 papers in edited books, international journals and conference proceedings, and has co-founded 9 spinoffs so far.

Foundations and Trends® is the name given to a series of publications that provide in-depth, comprehensive, state-of-the-art reviews on current and fundamental research topics. The uniqueness of Foundations and Trends® is that each published title is a long review, short monograph or set of overview articles (50-200 pages) on a topic, written by one or more recognized experts in the field and thoroughly peer-reviewed to ensure that the criteria for acceptance have been met. Foundations and Trends® are published using a journal model, with each journal its own Editor-in-Chief and Editorial Board. Together with the publisher these well-renowned academics identify the topics and the authors who should write on them. Each topic and author is carefully chosen to provide timeliness and clarity of

exposition. Each Foundations and Trends® publication puts primary research into context, improving researchers' and students' understanding of the original literature. Foundations and Trends® combine the peer review of journals, the high usage of reference works, and the pedagogy of textbooks. Foundations and Trends® have contributors in every country in the world conducting research, creating a new paradigm in publishing. Scientific rankings play a crucial part in the assessment of the quality of publications. Due to the careful topic selection process and diligent peer-review, all of the Foundations and Trends® publications can be found in the top ranking quartiles of their subject areas.

“Now Publishers is honored to be working with Georges Gielen to launch this new Foundations and Trends journal.” Mark de Jongh of Now Publishers said in a statement about the journal, “We believe that the survey and tutorial articles that we aim to publish in the journal will prove to be extremely useful to students and professionals starting out in these fields, as well as existing researchers whom need to get up to speed on the key literature. We are confident that under Georges' supervision, the new Foundations and Trends in Integrated Circuits and Systems is well supported and positioned to become a leading source of knowledge in Circuits and Systems and Solid-State Circuits.”

The first issues in 2021 are in preparation, including work from Klaas Bult (TU Delft and the most recent winner of the IEEE Donald O. Pederson Award in Solid-State Circuits), Subhasish Mitra (*Stanford University*), and Albert Theuwissen (*Harvest Imaging and TU Delft*).

Look for the first issue in early 2021 !